

This listing of claims will replace all prior versions, and listings, of claims in the application:

The Status of the Claims

1. (Cancelled)

2. (Cancelled)

3. (Cancelled)

4. (Cancelled)

5. (Previously Presented) A method of preserving exceptions in code reordering,
the method comprising:

receiving a plurality of instructions including a first instruction;

determining if the first instruction is an excepting instruction;

determining if the first instruction is to be moved upward across a check
instruction;

determining a second instruction in the plurality of instructions that computes
a previous value of a target register associated with the first instruction when the first
instruction is not an excepting instruction and the first instruction is to be moved upward
across a check instruction;

determining if a source operand associated with the second instruction is
available at the check instruction;

inserting a third instruction into the plurality of instructions to save the value

of the target register if the source operand associated with the second instruction is not available at the check instruction; and

inserting a fourth instruction into a recovery block to restore the value of the target register.

6. (Original) A method as defined in claim 5, further comprising inserting a copy of the second instruction into the recovery block if the source operand associated with the second instruction is available at the check instruction.

7. (Original) A method as defined in claim 6, wherein inserting a copy of the second instruction into the recovery block comprises inserting a copy of the second instruction into the recovery block ahead of a copy of the excepting instruction.

8. (Previously Presented) A method of preserving exceptions in code reordering, the method comprising:

receiving a plurality of instructions including a first instruction;
determining that the first instruction is an excepting instruction;
determining if the first instruction is to be moved upward across a check instruction;
determining if the first instruction is to be moved downward across the check instruction; and
inserting a copy of the first instruction into a recovery block in response to determining that (i) the first instruction is not an excepting instruction, (ii) the first instruction

is not to be moved upward across a check instruction, and (iii) the first instruction is to be moved downward across a check instruction.

9. (Original) A method as defined in claim 8, wherein inserting a copy of the first instruction into the recovery block comprises inserting a copy of the first instruction into the recovery block ahead of a copy of the excepting instruction.

10. (Cancelled)

11. (Cancelled)

12. (Cancelled)

13. (Cancelled)

14. (Cancelled)

15. (Cancelled)

16. (Cancelled)

17. (Cancelled)

18. (Cancelled)

19. (Cancelled)

20. (Previously Presented) A machine readable medium structured to cause a machine to:

receive a plurality of instructions including a first instruction;

determine if the first instruction is an excepting instruction;

determine if the first instruction is to be moved upward across a check instruction;

determine a second instruction in the plurality of instructions that computes a previous value of a target register associated with the first instruction when the first instruction is not an excepting instruction and the first instruction is to be moved upward across a check instruction;

determine if a source operand associated with the second instruction is available at the check instruction;

insert a third instruction into the plurality of instructions to save the value of the target register if the source operand associated with the second instruction is not available at the check instruction; and

insert a fourth instruction into a recovery block to restore the value of the target register.

21. (Original) A machine readable medium as defined in claim 20, wherein a copy of the second instruction is inserted in the recovery block if the source operand associated with the second instruction is available at the check instruction.

22. (Original) A machine readable medium as defined in claim 21, wherein a copy of the second instruction inserted in the recovery block comprises a copy of the second instruction inserted into the recovery block ahead of a copy of the excepting instruction.

23. (Previously Presented) A machine readable medium structured to cause a machine to:

receive a plurality of instructions including a first instruction;

determine if the first instruction is an excepting instruction;

determine if the first instruction is to be moved upward across a check instruction;

determine if the first instruction is to be moved downward across the check instruction; and

insert a copy of the first instruction into a recovery block in response to determining that (i) the first instruction is not an excepting instruction, (ii) the first instruction is not to be moved upward across a check instruction, and (iii) the first instruction is to be moved downward across a check instruction.

24. (Original) A machine readable medium as defined in claim 23, wherein a copy of the first instruction inserted into the recovery block comprises a copy of the first instruction inserted into the recovery block ahead of an excepting instruction.